

# Recruits! 研究生/五年一貫生/專題生 招募中

## 研究方法 Research Approach:

訊號處理演算法開發

Signal Processing Algorithms

數位系統架構設計

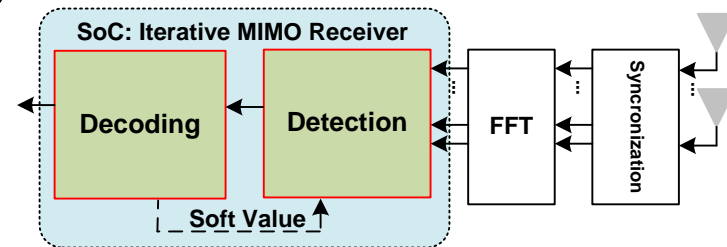
Digital System Architecture

超大型積體晶片佈局

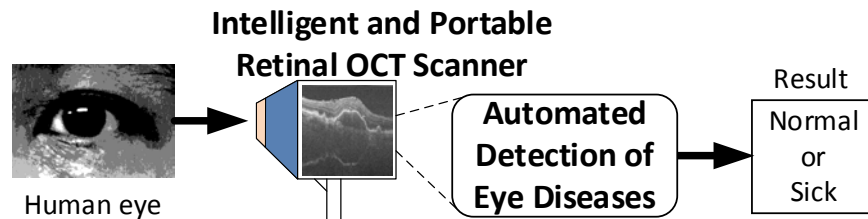
VLSI Chip Layout

## 研究專題 Research Topics:

(1) 通訊應用:

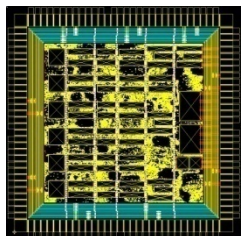
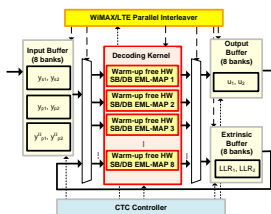


(2) 醫療器材電子應用:



場可編輯邏輯閘陣列驗證

FPGA Verification



# 技能與潛能 (Skills & Potentials)

- 相關知識 (Fundamentals):
  - Logic Circuit Design, Digital Systems, Signals and Systems, Digital Signal Processing, Computer Organization and Architecture, VLSI Circuits and Systems, Probabilities, Communication Systems
- 工作技能 (Skills):
  - C/C++, MATLAB, Verilog HDL
- 使用軟體 (Tools):
  - Visual C++, MATLAB, NC Verilog, Debussy, Design Vision, Verification Navigator, TurboScan, IC Compiler, Calibre
- 技術竅門 (Know-how):
  - 從數位信號處理演算法開發到硬體架構設計  
From digital signal processing algorithms to digital architectures
  - 從硬體架構設計到晶片硬體實作  
From digital architectures to integrated chips
- 可能工作 (Potential Jobs):
  - IC Design House: 聯發科, 聯詠, 智原, 創意, 原相, ...
  - System Company: 華碩, 宏達電, 明基, 宏碁...