

◎ Prerequisites (Preferred)

1. 邏輯電路設計、程式語言、資料結構、機率學、線性代數、
2. 計算機組織與架構、信號與系統、通訊系統、超大型積體電路導論、數位訊號處理

◎ Skills

C 語言、MATLAB、Verilog HDL、SPICE

◎ The graduation requirement

M.S. Degree

1. Defense Ticket: One of the following items **with a Demonstration** (e.g. chip layout with CIC review form or FPGA board)
 - a、An International Conference Paper
 - b、A Contest Report
 - c、With my approval, IEEE 2-column paper draft over 8 pages
2. A thesis in English with a passed oral defense
3. IEEE 2-column paper draft (over 8 pages)
4. A graduate disk

Ph.D. Degree

1. Defense Ticket:
 - a、Two International Conference Papers
 - b、Two SCI Journal Papers
 - c、Two Contest Reports with two Demonstrations (e.g. chip layout with CIC review form or FPGA board)
2. A dissertation in English with a passed oral defense
3. A graduate disk

Edited by Prof. Cheng-Hung Lin (2013/1/5)